

ECE 204L - COUNTERS - LAB 22

DESIGN OF SYNCHRONOUS COUNTERS

WINTER 2004

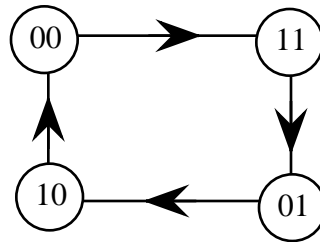
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OBJECTIVE

The objective of this lab is to design, build and test a synchronous counter.

LAB

1. Design, build and test a synchronous counter with D flip-flops to implement the following state diagram



Document your design and test results. Make use of CLEAR to start the counter off in the zero state